

2EN 4kOpen Board HW Application note

How to add 1V8 data and control signal on SD card

Introduction

The HW of 4K UltraHD H.265 / HEVC community board based on STiH418 for the 4kOpen platform contain a SD card slot connecteur. Based on B2264B refrence design, the SD card data and control signals at 3V3.

The purpose of this document is to describe the HW update to support swichable 3V3 and 1V8 data and control signal on SD card $\,$

Scope

This document is targeted at the following audiences in the context of 4kOpen project:

- Developers of HW platform similar to 4kOpen with STiH418
- Developers of SW for 4kOpen platform



Below is a list of documents that should be consulted alongside the present document.

Table 1 Reference

| # | Document name | Document description |
|-----|--------------------|-----------------------|
| [1] | STiH418 data sheet | Data Sheet of the SOC |



cx577133 1/7



Acronyms and Abbreviations

B2264 4kOpen reference board HDK Hardware Development Kit

HW Hardware N/A Not Applicable NC Not Connected

SOC System On Chip (STiH418)

SW Software

Rel. 1.1 Page 2/7



Contents

| 1 | SC CARD interfcae on 4Kopen B2264B | 4 |
|---|--|---|
| | | |
| 2 | Add 1V8 data control capability on SC CARD | 6 |
| | | |
| 3 | Revision history | 7 |



PIO42_4 PIO42_5

STiH318

SC CARD interfcae on 4Kopen B2264B

The SD card on 4Kopen B2264B board is controlled from the SOC with signals summarized on Table 2.

PIO Signal Description SoC power Voltage domain PIO40 6 **SDCLK** Clock to SD Card PIO40 7 **SDCMD** Command to SD Card PIO41 0 SDD0 SD Card data0 PIO41 1 SDD1 SD Card data1 CPU FLASH C PIO41 2 SDD2 SD Card data2 3V3 SD Card data3 PIO41 3 SDD3 PIO42 2 SD PWREN 3V3 Power enable not

Table 2: SD card signals on 4Kopen B2264B board

The Figure 1 to Figure 3 shows the schematics of the SD card schematics on 4Kopen B2264B board

Write protection, fiex to ground

SD card detection

1V8 and 3V3 IO Tolerance

SDCARD_DETECT

SD WP

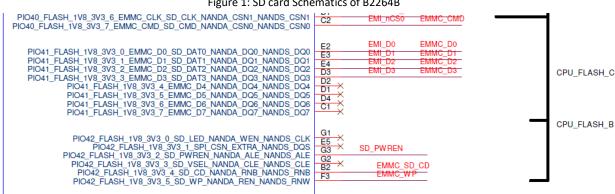
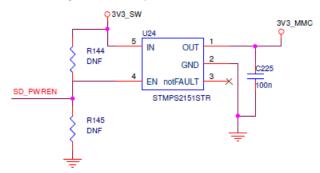


Figure 1: SD card Schematics of B2264B

| EMMC_WP | SD_WP | |
|-------------------|----------|---------------|
| EMMC_SD_CD | R137 33R | SDCARD_DETECT |
| EMMC_CLK | R138 33R | SDCLK |
| EMI_nCS0 EMMC_CMD | R139 33R | SDCMD |
| EMI_D0 EMMC_D0 | R140 33R | SDD0 |
| EMI_D1 EMMC_D1 | R141 33R | SDD1 |
| EMI_D2 EMMC_D2 | R142 33R | SDD2 |
| EMI_D3 EMMC_D3 | R143 33R | SDD3 |

Figure 2: SD card power enable on B2264B



Rel. 1.1 Page 4/7

CPU_FLASH_B



ECC13-S301AAA0

Figure 3: SD card connector on B2264B

Rel. 1.1 Page 5/7



2 HW modification to add 1V8 data control capability on SC CARD

The modification on schematics to add the capability of switching the data and control signals of the SD card between 3V3 anto 1V8 is depicted on Figure 4 .

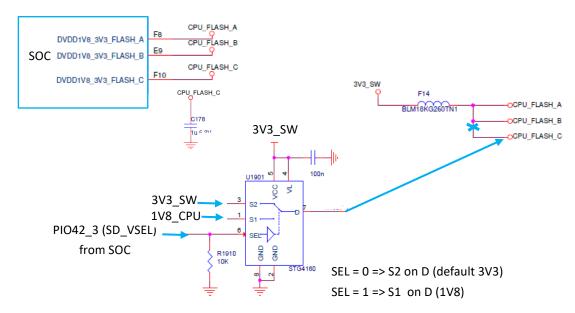


Figure 4: Schematics change form B2264B to add 3V3 /1V8 SD card data control capability

The Table 3 provides the changes on signals and voltage;

| PIO | Signal | Description | SoC power domain | Voltage |
|---------|---------------|-----------------------------------|------------------|------------------|
| PIO40_6 | SDCLK | Clock to SD Card | | |
| PIO40_7 | SDCMD | Command to SD Card | | |
| PIO41_0 | SDD0 | SD Card data0 | | |
| PIO41_1 | SDD1 | SD Card data1 | CPU_FLASH_C | 3V3 if PIO42_3=0 |
| PIO41_2 | SDD2 | SD Card data2 | | 1V8 if PIO42_3=1 |
| PIO41_3 | SDD3 | SD Card data3 | | |
| PIO42_2 | SD_PWREN | 3V3 Power enable not | | |
| PIO42_3 | SD_VSEL | 1V8 V selection | | |
| PIO42_4 | SDCARD_DETECT | SD card detection | CPU_FLASH_B | 3V3 |
| PIO42_5 | SD_WP | Write protection , fiex to ground |] | |

Table 3: SD card signals on 4Kopen after updateB2264B board



3 Revision history

Table 4: Document revision history

| Date | Revision | Changes |
|--------------|-----------|-----------------|
| 2 march 2018 | Draft 1.0 | Initial version |